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LSI Logic Corporation Legal Department – IP 1621 Barber Lane, MS D-106 Milipitas, CA 95035

| Date | October 20, 2004 | · |
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| REMARKS: | | | | | · · · · · · · · · · · · · · · · · · · | | |
|---|--|---|------------|--|---------------------------------------|--|--|
| . Urge | ent | 1 | Reply ASAP | | Please comment | | |
| Filing date: First named inventor: | 10/642,706 August 18, 2003 Chok Chia et al. 00-282/1D / 68510 | | | | | | |
| Transmitted herewith for filing via facsimile: • Status Request Letter | | | | | | | |
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PAGE 1/2 * RCVD AT 10/20/2004 1:18:24 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/2 * DNIS:8729306 * CSID:408 433 7460 * DURATION (mm-ss):00-56

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

§ §

In re Application of:

Chok Chia et. al.

Serial No.:

10/642,706

10:14

Filed:

August 18, 2003

For:

Insulated Bonding Wire Tool for

Microelectronic Packaging

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Group Art Unit:

2827

OCT 2 0 2004

Examiner:

Graybill, D.

Atty Docket:

68510 / 00-282/1D

STATUS REQUEST

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 [+1] 408-433-7191

Respectfully submitted,

Henry Groth, Ph: [+1] 408-433-7191

Reg. No. 39,696